**Assignment 4**

All assignments are to be submitted strictly before start of next lab session through online only. Late assignments will not be entertained and will be awarded ‘0’ marks.

Copy-paste or type the unique URL of your assignment solution from website [www.edaplayground.com](http://www.edaplayground.com) for assignment questions. Please note that do not copy someone else’s link as any kind of unfair means will result in academic misconduct and will be treated accordingly. All links for each user and each code are unique.

**Q1:** Verilog code and testbench for converting 8-bit binary number to gray code using data flow modeling.

**Ans: Link1: https://www.edaplayground.com/x/smke**

**Q2:** Verilog code and testbench for full subtractor using data flow modeling.

**Ans: Link2: https://www.edaplayground.com/x/6wJs**

**Q3:** Verilog code and testbench for full subtractor using behavioral modeling.

**Ans: Link3: https://www.edaplayground.com/x/6wK3**